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Total Number of Pages in This Submission

6

Application Number

09/746,602

Filing Date

12-21-2000

First Named Inventor

James S. McCormick, et al.

Art Unit

2661

Examiner Name

Vanderpuye, Kenneth N.

Attorney Docket Number

1400.4100220

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Ross D. Snyder & Associates, Inc.		
Signature			
Printed name	Ross D. Snyder		
Date	07/05/2005	Reg. No.	37,730

CERTIFICATE OF TRANSMISSION/MAILING

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): James S. McCormick, et al.

Title: MULTIPROCESSOR CONTROL BLOCK FOR USE IN A
COMMUNICATION SWITCH AND METHOD THEREFORE

App. No.: 09/746,602

Filed: 12-21-2000

Examiner: Vanderpuye, Kenneth N. Group Art Unit: 2661

Atty. Dkt. No. 1400.4100220

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE

Dear Sir:

In response to the Office action of May 5, 2005, Applicant submits the following:

REMARKS/ARGUMENTS

Claims 1-31 are pending in the present application. The Examiner has allowed claims 1-23. The Examiner has rejected claims 24-27. The Examiner has objected to claims 28-31. Applicant respectfully requests reconsideration of pending claims 24-31.

The Examiner has rejected claims 24, 25, and 27 under 35 U.S.C. § 102(b) as being anticipated by Osaki (U.S. Patent No. 5,657,449). Applicant respectfully disagrees.

In the Examiner's Response to Arguments, the Examiner states as follows:

"The examiner makes no reference to line setter 211 or call setter 212 in the rejection. It is not clear why applicant has focused his arguments on these two components. The examiner cited 'line setting processors' in the preamble as the link layer processor however it is clear from the body of the reject that the examiner was referring to the 'path management processor.' In the previous rejection and in this rejection the 'line setter processors' in the body of the claim are cited as being intermediate processors. The examiner has directed the applicant to exact portions of the reference that support the rejection. Applicant is advised to read col. 9 line 47-col. 10 line 32."